

Figure 2

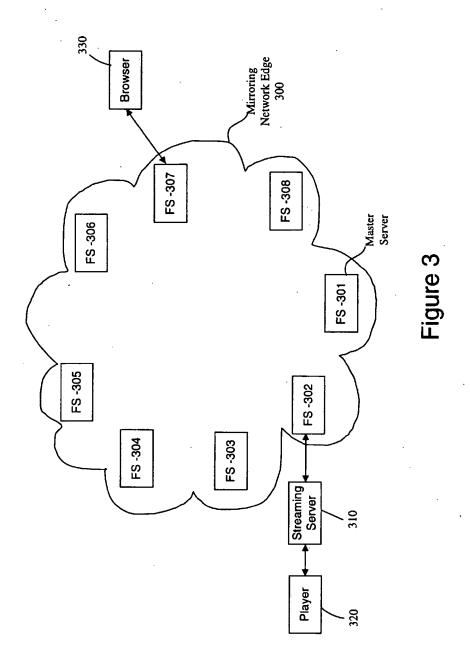
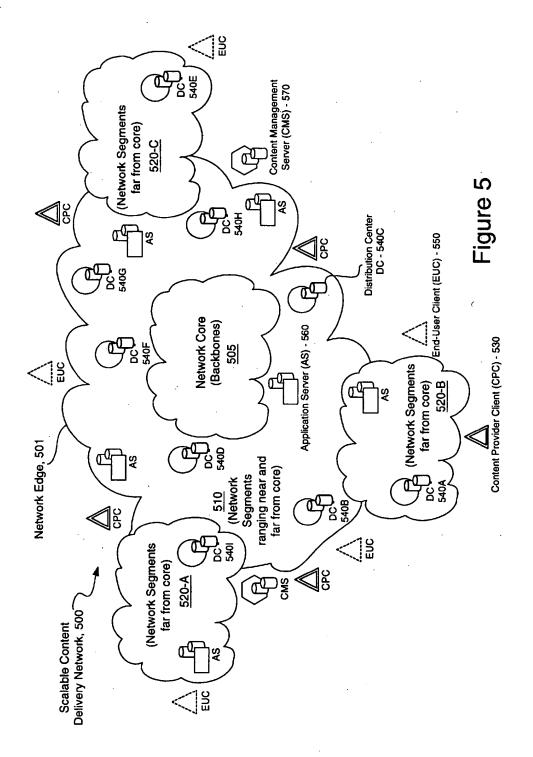
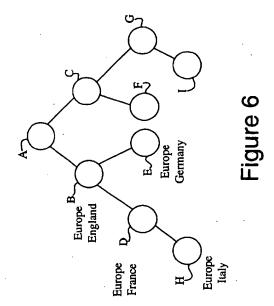
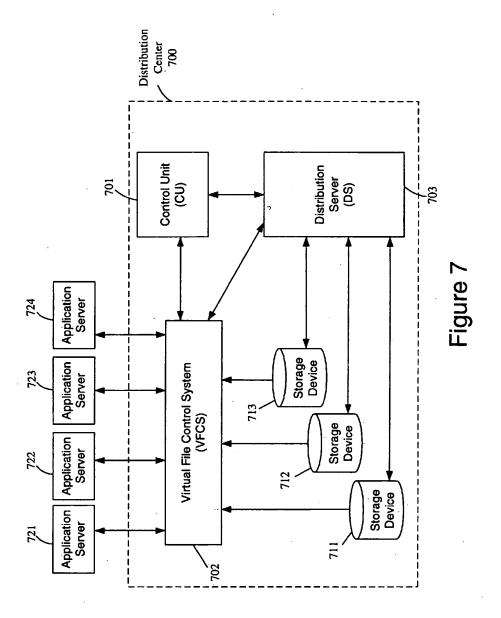


Figure 4







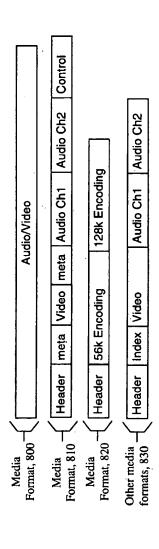
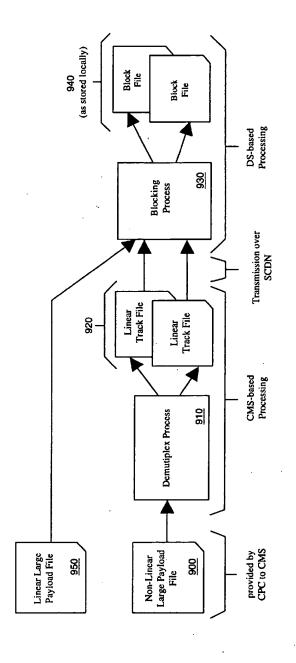
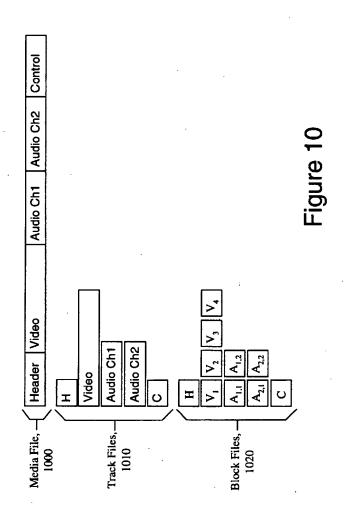


Figure 8



Figure



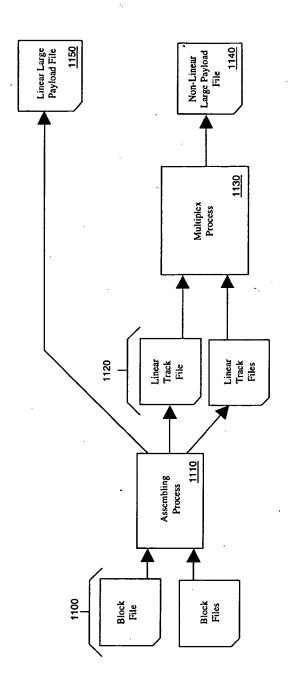


Figure 11

1200		1210	1220		1230	1240		1250
16	0	<u> </u>	0					
15	0	0	-		0	0		
14	0	-	0		0	0		-
13		0	0	•	0	0		0
12	0	0	0		0	0		0
=	0	0	0		0	0		0
10	0	0	0		0	0		0
6	0	0	0		0	0		0
∞	. 0	0	0		0	0		0
7	0	0	0		0	0		0
9	0	0	0		0	0		0
5	0	0	0		0	0		0
4	1	1	1		1	1		1
9	0	0	0		0	0		0
2	0	0	0		0	0		0
	1		1			-		-
Bit Number	Attribute B	Attribute D	Attribute E		Attribute H	Rolled Up Attribute D	ì	Rolled Up Attribute B

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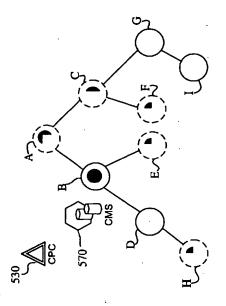
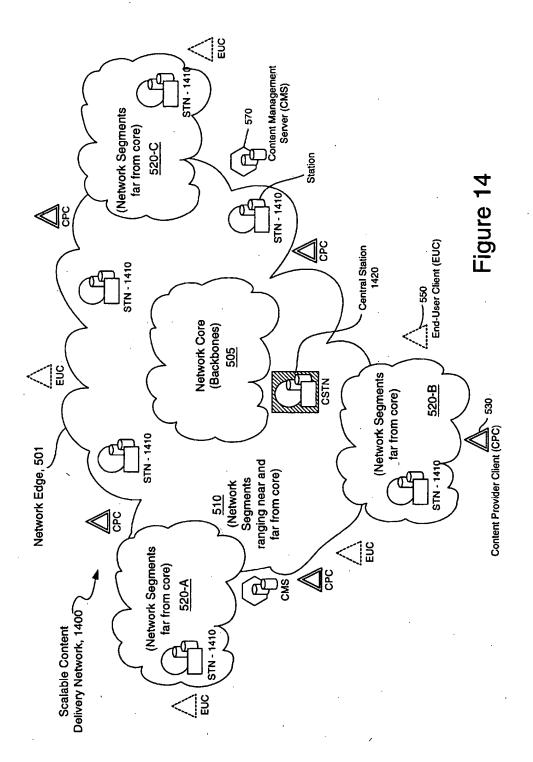


Figure 13



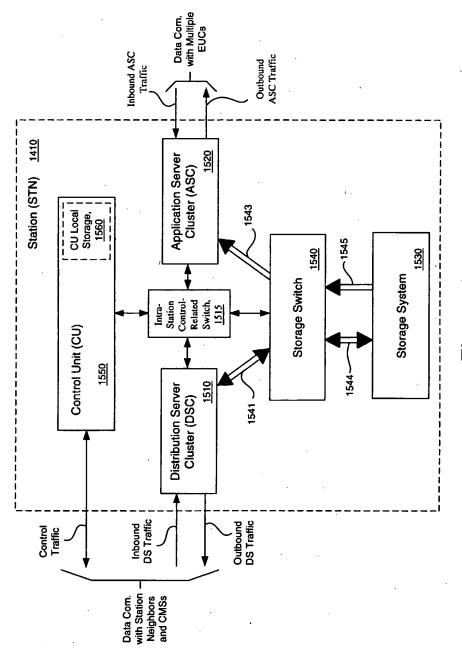
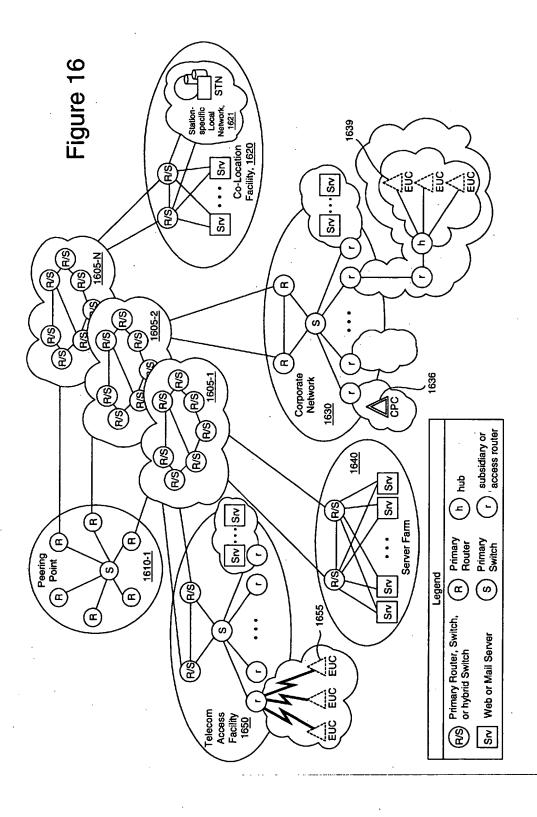
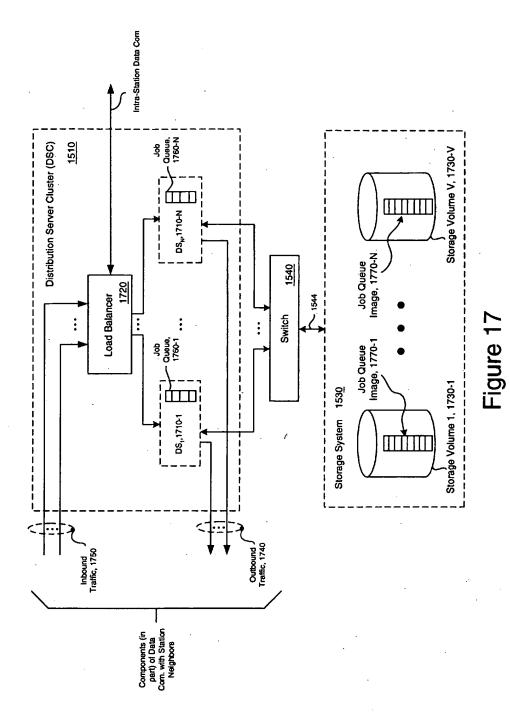
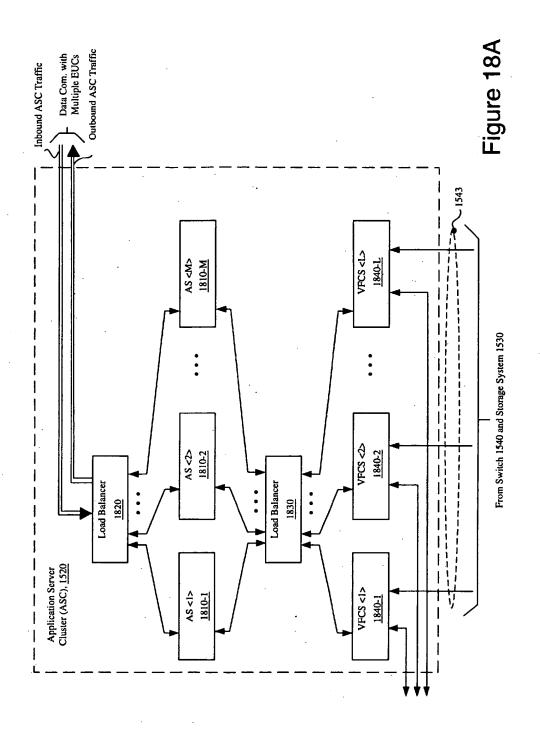
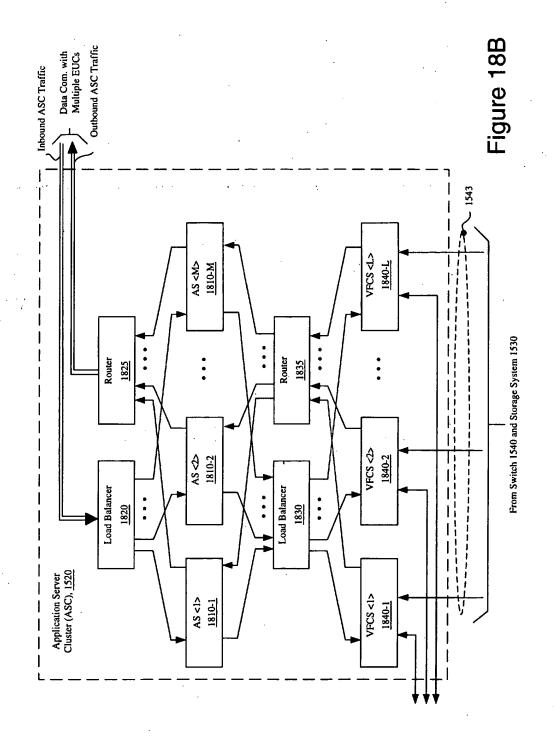


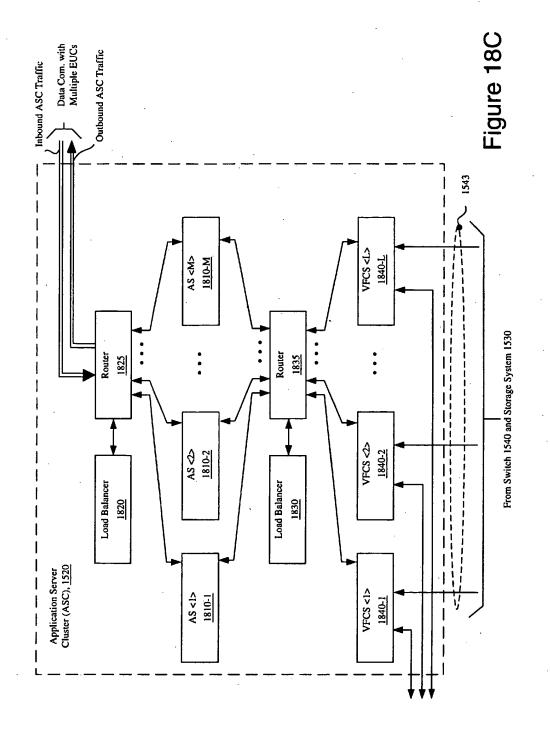
Figure 15

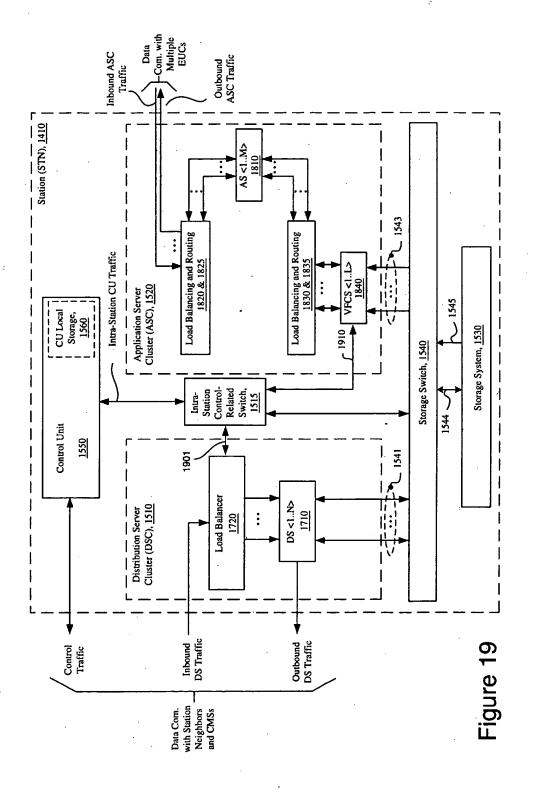












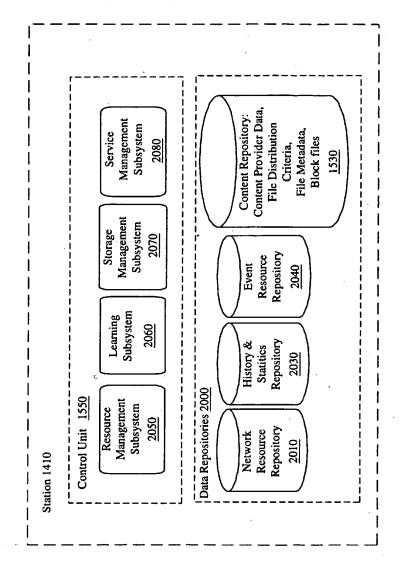
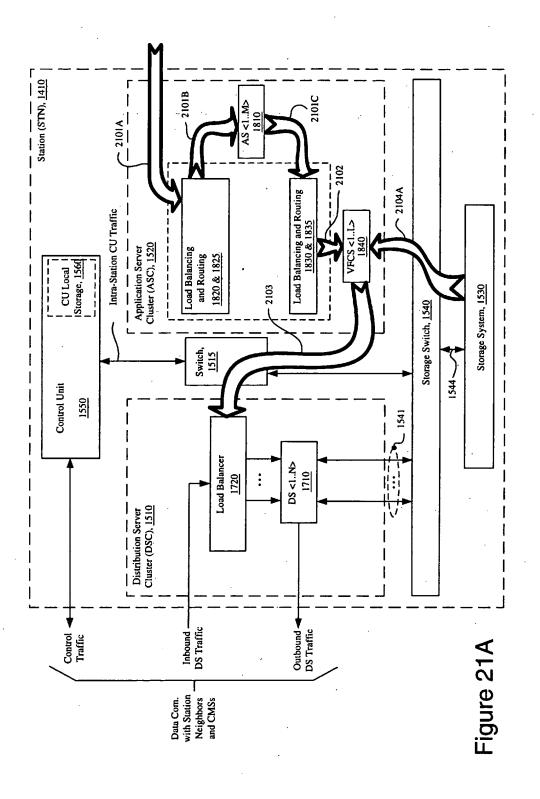
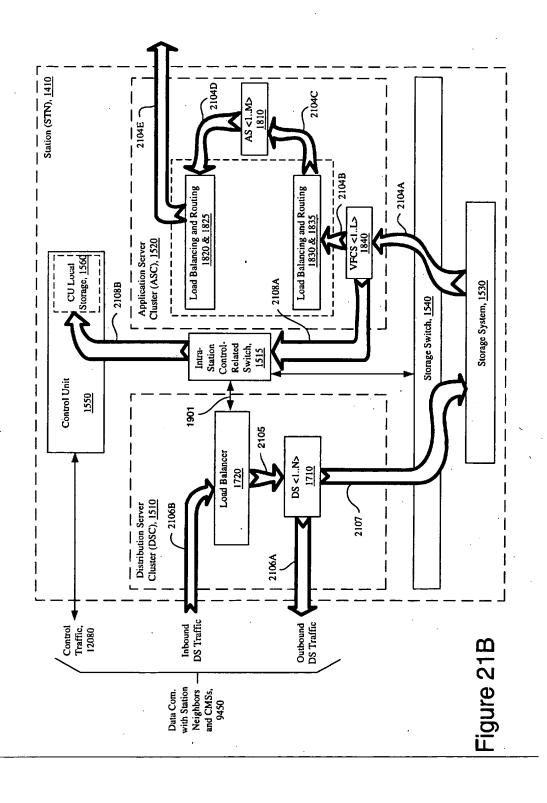
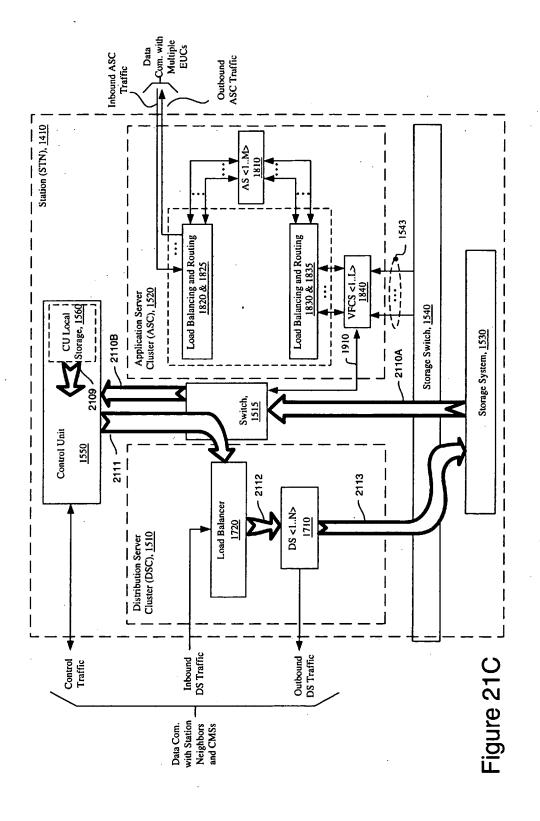
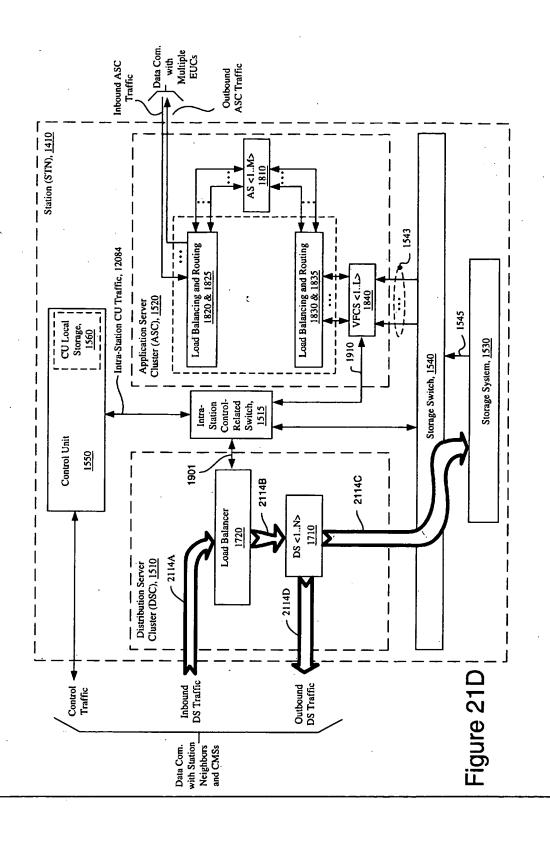


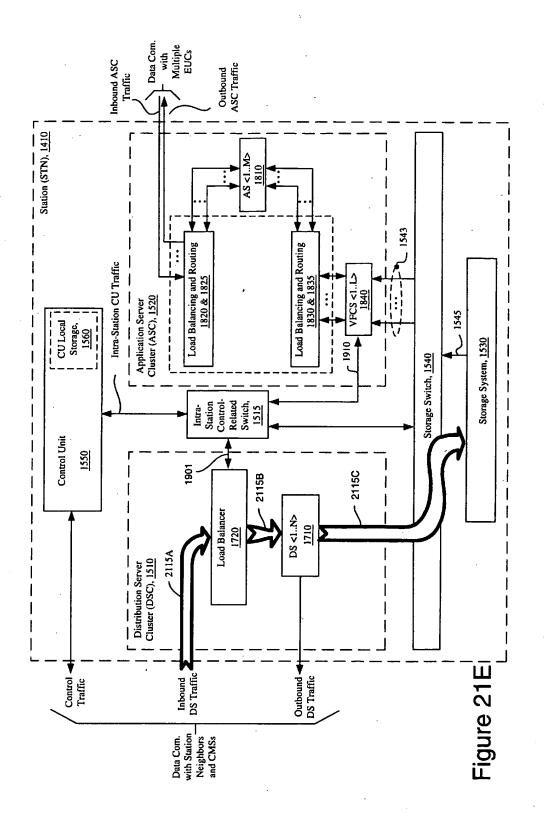
Figure 20

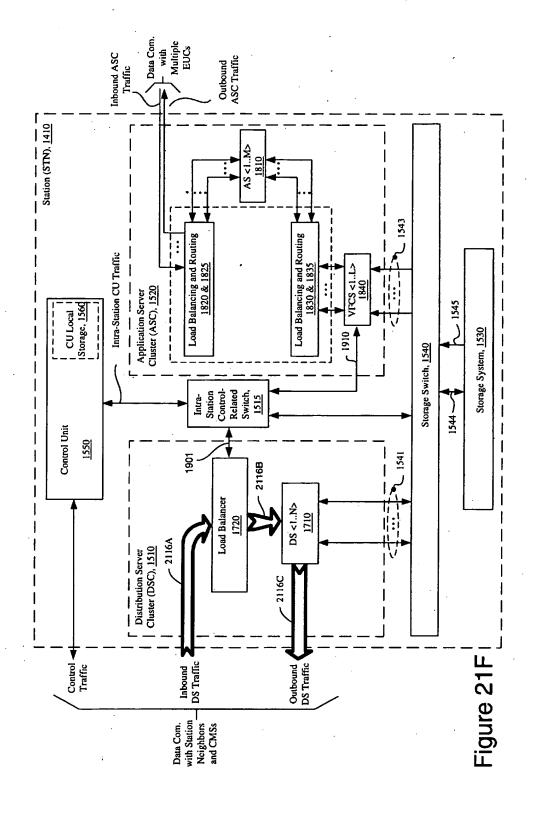


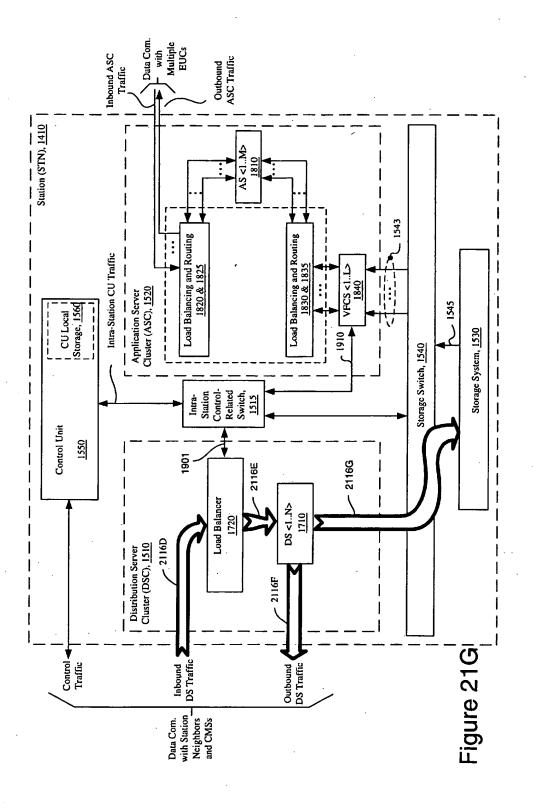


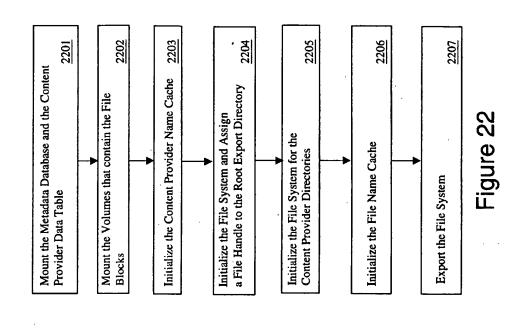












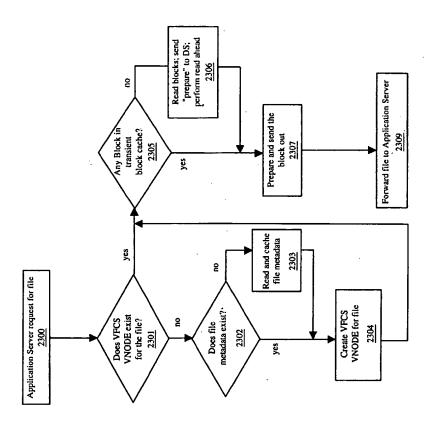


Figure 23

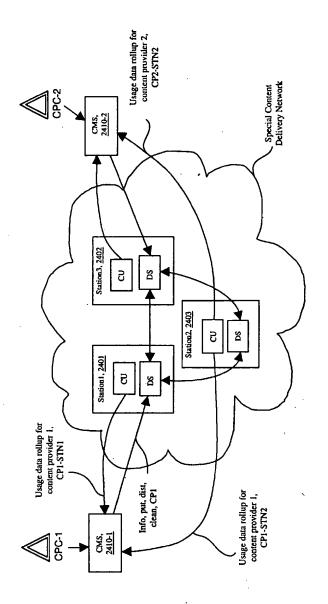


Figure 24

